## Contact

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## Top Skills

Statistical Inference
Data Analysis
Data Visualization

# Weiping Yan

Electrical Engineer
Amsterdam, North Holland, Netherlands

## Summary


Undergraduate student in a joint program between TU Delft and Eindhoven University of Technology, studying Computer Science, Electrical Engineering, and Applied Physics. My research interests lie in AI for Science, foundation models, and AI-driven approaches for analog circuit design and optimization. I am particularly interested in how modern machine learning methods can be applied to model, optimize, and automate the design of analog circuits, as well as to interact with complex physical systems.With an interdisciplinary background spanning software, hardware, and applied physics, I enjoy working on problems that bridge intelligent algorithms with real-world engineering systems. I am currently seeking research and collaboration opportunities in AI for Science, foundation models, and AI-driven analog circuit design and optimization. I also plan to continue my undergraduate studies in the United States in the near future.


## Experience

### The Chinese University of Hong Kong
Research Assistant
March 2026 - Present (1 month)
Shenzhen, Guangdong, China

LLM4Rec, AI4S

### Eindhoven University of Technology
Teaching Assistant
September 2025 - November 2025 (3 months)
Eindhoven, North Brabant, Netherlands

Assisted in teaching undergraduate courses in calculus and logic/set theory.

Led tutorial sessions focusing on problem-solving in limits, derivatives, and integrals, as well as formal reasoning and set operations.

Guided students through rigorous mathematical concepts and provided structured feedback to improve analytical thinking.

### Zhangjiang Laboratory
Research Assistant
July 2025 - September 2025 (3 months)
Shanghai, China

Researched the standard semiconductor IC design workflow and EDA toolchain, documenting the end-to-end process from schematic design and pre-layout simulation to layout verification and tape-out preparation.

Studied publicly available PDK concepts and design rule structures, focusing on how DRC constraints affect layout strategies and manufacturability.

Compiled technical notes explaining the roles of DRC/LVS verification, common failure modes, and debugging approaches in the sign-off stage.

Prepared an MPW-oriented tape-out workflow overview summarizing key milestones, verification checkpoints, and risk considerations from schematic sign-off to foundry submission.

### Carnegie Mellon University
Student Researcher
September 2023 - February 2024 (6 months)
Beijing, China

Conducted an independent environmental data research project under the joint supervision of Prof. Soummya Kar from Carnegie Mellon University and Associate prof. Qiang Gao from Southeast University.

Investigated the relationship between PM2.5 air pollution and respiratory disease incidence using environmental and public health datasets.

Constructed a structured dataset and performed exploratory data analysis and statistical inference using Python.

Applied data visualization techniques to identify temporal and regional trends in air pollution exposure and health outcomes.

## Education

### Delft University of Technology

Bachelor of Science, Science and Engineering · (September 2024 - June 2028)

### Eindhoven University of Technology

Bachelor of Science, Science and Engineering · (September 2024 - June 2028)

### The Affiliated High School to Hangzhou Normal University

High School Diploma, Chinese, Maths, English, Physics, Chemistry, Technology · (September 2021 - August 2024)

### Shanghai Foreign Language School Affiliated to SISU

Science and Engineering · (July 2023 - August 2024)

### Hangzhou New Channel - HUAER XINDA School

English, Maths, Science · (March 2023 - June 2023)